SOURCE	CONNECTION	DESTINATION
BBC(myri)		
	⇨TRGMYRI-3 (S2, P8)	
	⇕TRGMYRI-3 (S3, P15)	
	⇨ PLAT-MIC (MNET 3A/B)	
	⇨ MNET-MIC (DAQ 3A/B)	
	⇨TRGMYRI-1 (S4, P14)	
	⇕TRGMYRI-1 (S3, P11)	
		⇨ L2ANA01(PCI2)
BBC(stp)		
	⇨CONCENTRATOR (P2)	
	⇕CONCENTRATOR (P3)	
	⇨ PLAT-MIC (MNET 4A/B)	
	⇨ MNET-MIC (DAQ 4A/B)	
		⇨ L2ANA01(PCI1)
BC1(myri)		
	⇨TRGMYRI-3 (S2, P10)	
	⇕TRGMYRI-3 (S3, P15)	
	⇨ PLAT-MIC (MNET 3A/B)	
	⇨ MNET-MIC (DAQ 3A/B)	
	⇨TRGMYRI-1 (S4, P14)	
	⇕TRGMYRI-1 (S3, P11)	
		⇨ L2ANA01(PCI2)
BC1(stp)		
	⇨CONCENTRATOR (P2)	
	⇕CONCENTRATOR (P3)	
	⇨ PLAT-MIC (MNET 4A/B)	
	⇨ MNET-MIC (DAQ 4A/B)	
		⇨ L2ANA01(PCI1)
BCE(myri)		
	⇨TRGMYRI-3 (S2, P11)	
	⇕TRGMYRI-3 (S3, P15)	
	⇨ PLAT-MIC (MNET 3A/B)	
	⇨ MNET-MIC (DAQ 3A/B)	
	⇨TRGMYRI-1 (S4, P14)	
	⇕TRGMYRI-1 (S3, P11)	
		⇨ L2ANA01(PCI2)
BCE(stp)		
	⇨CONCENTRATOR (P2)	
	⇕CONCENTRATOR (P3)	
	⇨ PLAT-MIC (MNET 4A/B)	
	⇨ MNET-MIC (DAQ 4A/B)	
		⇨ L2ANA01(PCI1)

BCW(myri)

⇨TRGMYRI-3 (S2, P9)
⇕TRGMYRI-3 (S3, P15)
⇨ PLAT-MIC (MNET 3A/B)
⇨ MNET-MIC (DAQ 3A/B)
⇨TRGMYRI-1 (S4, P14)
⇕TRGMYRI-1 (S3, P11)
⇨ L2ANA01(PCI2)

BCW(stp)

⇨CONCENTRATOR (P2)
⇕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (DAQ 4A/B)
⇨ L2ANA01(PCI1)

CTB(myri)

⇨TRGMYRI-2 (S2, P10)
⇕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
⇕TRGMYRI-1 (S3, P11)
⇨ L2ANA01(PCI2)

CTB(stp)

⇨CONCENTRATOR (P2)
⇕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (DAQ 4A/B)
⇨ L2ANA01(PCI1)

FPE(myri)

⇨TRGMYRI-4 (S2, P8)
⇕TRGMYRI-4 (S3, P13)
⇨ EAST-MIC (MNET 1A/B)
⇨ MNET-MIC (E.MNET 1A/B)
⇨TRGMYRI-1 (S4, P11)
⇕TRGMYRI-1 (S3, P11)
⇨ L2ANA01(PCI2)

FPE(stp)

⇨ PMD-MIC (E.PLAT 4A/B)
⇨ PLAT-MIC (E.PLAT 4A/B)
⇨CONCENTRATOR (P2)
⇕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (MNET 4A/B)

⇨ L2ANA01(PCI1)

FPW(myri)

⇨TRGMYRI-5 (S2, P8)
⇕TRGMYRI-5 (S3, P12)
⇨ WEST-MIC (FPD W 1A/2A)
⇨ MNET-MIC (W.MNET 1A/2A)
⇨TRGMYRI-1 (S3, P15)
⇕TRGMYRI-1 (S3, P11)

⇨ L2ANA01(PCI2)

FPW(myri)

⇨ EEMC-MIC (W.PLAT 4A/B)
⇨ PLAT-MIC (W.PLAT 4A/B)
⇨CONCENTRATOR (P2)
⇕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (MNET 4A/B)

⇨ L2ANA01(PCI1)

L0(myri)

⇨TRGMYRI-2 (S2, P13)
⇕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
⇕TRGMYRI-1 (S3, P11)

⇨ L2ANA01(PCI2)

L0(stp)

⇨CONCENTRATOR (P2)
⇕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (DAQ 4A/B)

⇨ L2ANA01(PCI1)

L1(myri)

⇨TRGMYRI-2 (S2, P8)
⇕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
⇕TRGMYRI-1 (S3, P11)

⇨ L2ANA01(PCI2)

L1(stp)

⇨CONCENTRATOR (P2)
↕CONCENTRATOR (P3)
⇨ PLAT-MIC (MNET 4A/B)
⇨ MNET-MIC (DAQ 4A/B)
⇨ L2ANA01(PCI1)

RCC(myri)

⇨TRGMYRI-2 (S2, P9)
↕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
↕TRGMYRI-1 (S3, P11)
⇨ L2ANA01(PCI2)

RCC(clock)

⇨ SingleMode MAU
⇨ PLAT-MIC (MNET 6A/B)
⇨ RHIC-MIC (singlemode 1,2,3,4)
⇨ V124(P?)

Scaler(myri)

⇨TRGMYRI-2 (S2, P12)
↕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
↕TRGMYRI-1 (S3, P14)
⇨ L2ANA02(PCI3)

TCD(myri)

⇨TRGMYRI-2 (S2, P15)
↕TRGMYRI-2 (S3, P13)
⇨ PLAT-MIC (MNET 1A/B)
⇨ MNET-MIC (DAQ 1A/B)
⇨TRGMYRI-1 (S4, P15)
↕TRGMYRI-1 (S3, P11)
⇨ L2ANA01(PCI2)

TCD(glink1)

⇨GLINK (P2)
↕GLINK (P3)
⇨ PLAT-MIC (MNET 6A/B)
⇨ RHIC-MIC (multi 1A/B)
⇨ GLINK()

Scaler(glink2)

⇨CONCENTRATOR (P2)

⇕CONCENTRATOR (P3)

⇨ PLAT-MIC (MNET 6A/B)

⇨ RHIC-MIC (multi 6A/B)

⇨ L2ANA01(PCI1)

TRGMYRI-2 (S3, P14)

⇨TRGMYRI-3 (S3, P8)

key:

L1(myri) = pmc myrinet card in L1 processor in 1A4 rack, 2nd crate from top

TRGMYRI-2(S3,P14)N = MYRINET SWITCH IN 1A4 RACK; 3RD SLOT(OR BLADE) COUNTING FROM TOP; PORT NUMBERED 14

MNET-MIC (DAQ 1A/B) = MIC BOX LABELED “MNET” IN DC3 RACK; FIBER PANEL LABLED “DAQ”; ST CHANNELS 1A AND 1B

L2ANA01 (PC2) = CONNECTED TO PCI CARD IN 2ND SLOT COUNTING FROM LEFT

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